

Docket No.: Z&PINFP08190



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Date: 8/16/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Giuseppe Curello et al.
Applic. No. : 09/904,360
Filed : July 12, 2001
Title : Process for Producing a Doped Semiconductor Substrate

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,843,829 (Kuramae et al.), dated December 1, 1998;

United States Patent No. 6,048,781 (Turner et al.), dated April 11, 2000;

European Patent Application EP 0 889 502 A2 (Cardone et al.), dated January 7, 1999.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicants

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Date: August 16, 2001

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